



State Council of Educational Research and Training

(An autonomous body under Department of Education, Govt. of NCT of Delhi)

Varun Marg, Defence Colony, New Delhi 110024

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F 3(2)/ET/INSET/PGTCS/SCERT/2026-27/5751-59

Date: 13.08.2026

Circular

Subject: Two-days Capacity Building Programme for PGT Computer Science of the Directorate of Education.

The State Council of Educational Research and Training (SCERT), Delhi, as the State Academic Authority, is dedicated to continuously enhancing the quality of school education through sustained professional development and capacity building of teachers. Recognising the rapidly evolving nature of Computer Science and the growing importance of computational thinking, programming, data literacy, Artificial Intelligence, and emerging technologies in school education, SCERT Delhi is organising a Capacity Building Programme for Post Graduate Teachers (PGTs) of Computer Science working under the Directorate of Education, Government of NCT of Delhi.

The programme aims to strengthen the conceptual understanding, technical competencies, and practical proficiency of PGT Computer Science teachers and enable them to effectively address the requirements of the Senior Secondary Computer Science curriculum. The training will adopt a hands-on, experiential, and practice-oriented approach, providing teachers with opportunities to engage in coding activities, practical exercises, demonstrations, problem-solving, and application-based learning.

The programme will focus on key curriculum-aligned areas, including Python Programming, Data Handling using Python, Structured Query Language (SQL), Database Management, Computer Networks, Artificial Intelligence, and emerging technologies. Emphasis will be placed on strengthening programming and computational skills and connecting theoretical concepts with practical classroom applications.

Through this initiative, SCERT Delhi seeks to empower PGT Computer Science teachers with contemporary knowledge and practical skills, enabling them to adopt more effective and engaging pedagogical practices and facilitate meaningful learning experiences for students. The programme will also contribute towards developing a pool of competent, confident, and future-ready Computer Science teachers capable of keeping pace with the rapidly changing technological landscape and preparing students for emerging opportunities in the field of technology.

The program details are as under:

The training is to be organized in **two cycles** as mentioned below:

Cycle 1	Cycle 2
Date: 18 th -19 th August, 2026	Date: 24 th -25 th August, 2026
Venue: 10 Different Venues (List Attached)	Venue: 10 Different Venues (List Attached)
Reporting Time : 9:00 am	Reporting Time : 9:00 am
Time: 9:30 am to 4:00 pm	Time: 9:30 am to 4:00 pm

All Computer Science PGTs are hereby informed to attend the training positively and also it is mandatory to bring their Tab/Laptop.

List of all the participants is attached as **Annexure -I and II** with this circular.

Both the Annexures are available on the following link:

Annexure-I Cycle 1: 18th-19th August 2026:

<https://drive.google.com/file/d/1nHuJUyJNx5U qt-Q54lnYiFqhEud95AZ/view?usp=sharing>

Annexure- II: Cycle 2: 24th -25th August 2026:

<https://drive.google.com/file/d/147xYL5bjnj-DVzSHOL5NofNIGcGAt7aa/view?usp=sharing>



(Dr. Mukesh Yadav)
Seceratory, SCERT

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3. PS to Director, SCERT Delhi.
4. Additional DE (Schools), DoE
5. All RDE's/ DDE's (Districts and Zones), DoE
6. Dr. Sapna Yadav, Programme Director, ICT in Education, SCERT Delhi
7. Nodal officer, INSET Cell, SCERT
8. OS (IT to upload on MIS)
9. WIM, SCERT
10. Guard file